

Notice of References Cited

Application/Control No.

09/699,466

Applicant(s)/Patent Under
Reexamination
YAMAZAKI ET AL.

Examiner

Asok K. Sarkar

Art Unit

2813

Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number		Date MM-YYYY	Name	Classification	
		Country Code-Number-Kind Code					
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	B	US-4,330,363-		05-1982	Biegesen et al.	117	54
	C	US-4,466,073-		08-1984	Boyan et al.	356	400
*	D	US-5,550,070-		08-1996	Funai et al.	438	486
	E	US-6,180,439-B1		01-2001	Yamazaki et al.	438	162
	F	US- -					
	G	US- -					
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	I	US- -					
	J	US- -					
	K	US- -					
	L	US- -					
	M	US- -					

FOREIGN PATENT DOCUMENTS

*		Document Number		Date MM-YYYY	Country	Name	Classification	
		Country Code-Number-Kind Code						
	N	- -						
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	R	- -						
	S	- -						
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Gandhi, VLSI Fabrication Principles, John Wiley and Sons, p. 388, 1983.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.